



The 38th Annual IEEE/IFIP International Conference on Dependable Systems and Networks

June 24-27, 2008 — Anchorage, AK, USA

Second Workshop on Dependable and Secure Nanocomputing

Organizers:

Jean Arlat
LAAS-CNRS

Cristian Constantinescu
AMD

Ravishankar K. Iyer
UIUC

Michael Nicolaïdis
TIMA

Friday June 27, 2008

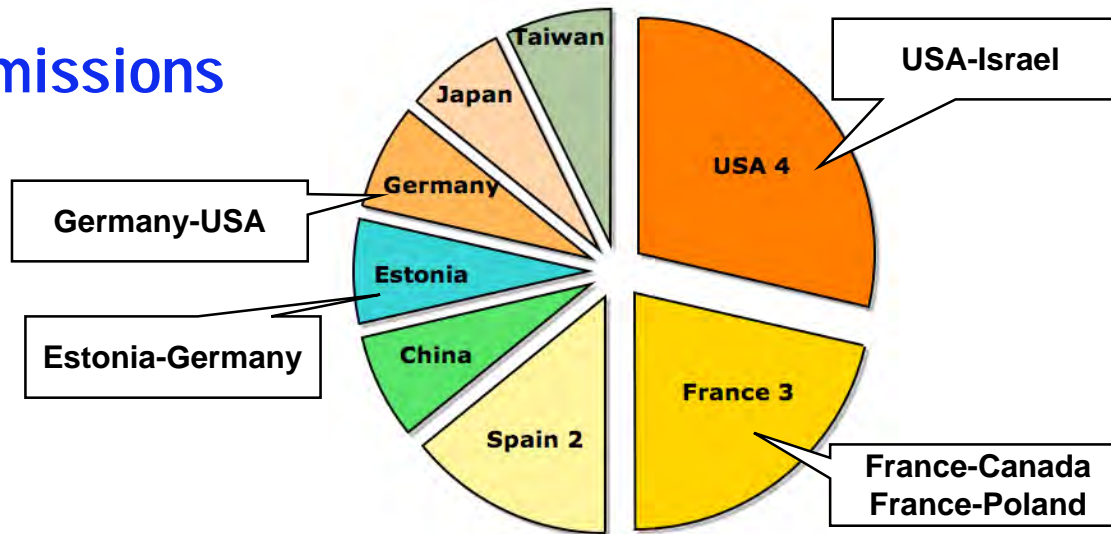
Program Committee

Jacob A. Abraham	University of Texas, Austin, USA
Lorena Anghel	TIMA, Grenoble, France
Pradip Bose	IBM T.J. Watson, Yorktown Heights, NY, USA
Jacques Collet	LAAS-CNRS, Univ. de Toulouse, France
Babak Falsafi	CMU, USA & EPFL, Switzerland
Subhasish Mitra	Stanford University, CA, USA
Shubhendu S. Mukherjee	Intel Corp., Hudson, MA, USA
Takashi Nanya	University of Tokyo, Japan
Rubin A. Parekhji	Texas Instruments, Bangalore, India
Ishwar Parulkar	Sun Microsystems, Santa Clara, CA, USA
Jean-Jacques Quisquater	Univ. Cath. Louvain, Belgium
Jaan Raik	Tallinn University of Technology, Estonia
Bruno Rouzeyre	LIRMM, Montpellier, France
Juan Carlos Ruiz García	Univ. Politécnica de Valencia, Spain
Matteo Sonza Reorda	Politecnico di Torino, Italy
Lisa Spainhower	IBM Server Group, Poughkeepsie, NY, USA
Vivian Zhu	Texas Instruments, Dallas, TX, USA
Yervant Zorian	Virage Logic, Fremont, CA



Program Set up

- 14 submissions



- Each submission assigned to 4 PC members
- Selection —> 13 presentations from submitted contributions: regular (20mn) and short (12mn)
- 1 invited talk

Program-at-a-Glance

1- Opening and Defect & Failure Modes in Nanoscale Technologies

Moderator

9-9:05 Workshop Introduction

9:05-9:30 Invited Talk: *Pia Sanda*

Ravi Iyer

9:30-10:30 Defect and Fault Models in Nanoscale Technologies

Alan Wood

David de Andrés, Nelson Tam, Raimund Ubar

10:30-11 Coffee

2- Performance and Security Issues in Hardware Design

11-11:40 Asynchronous Circuits

Nithin Nakka

Masashi Imai, Konrad J. Kulikowski

11:40-12:35 Intrusion Detection Devices

Daniel Sorin

Tien-Sheng Lin, Bruno Rouzeyre, Karthik Pattabiraman

12:35-14 Lunch

3- Mitigation and Resilience Techniques for Nanocomputing

14-14:35 Fault Tolerance Techniques

Juan Carlos Ruiz García

Lorena Anghel, Iliia Polian

14:35-15:10 Reconfigurable Nanoscale Circuits

Cristian Constantinescu

Jean Arlat, Mehdi Tahoori, David de Andrés

15:10:16 Workshop Wrap up

16 Adjourn

Regular - Short Presentations

