

The 10th International Symposium

Pacific Rim Dependable Computing

Papeete, Tahiti, French Polynesia March 3-5, 2004

http://www.laas.fr/PRDC10

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PRDC 2004 is the tenth in this series of symposia started in 1989, that are devoted to dependable and faulttolerant computing. PRDC is now recognized as the main regular event of the Pacific area that is covering the many dimensions of dependability and fault tolerance, encompassing fundamental theoretical approaches, practical experimental projects, and commercial components and systems.

In the recent years, the importance of dependability has been increasing beyond the classical critical application domains (telecommunication, transportation, space, etc.) as many other domains of the economy (commerce, finance, energy distribution) are recognizing dependability of the worldwide information and communication infrastructures to be one of their top problems. Besides traditional hardware and software faults, concerns include human interaction faults, being they accidental or malicious; the latter gaining increasing importance due to the evolution of Internet connections and applications.

The symposium will be organized in the archipelago of the Society Islands and more precisely in Tahiti. This central setting, right in the middle of the traditional zone of attraction of the symposium, will provide both a really convenient and most pleasant location for the researchers and practitioners in the domain of dependable computing. PRDC 2004 will be held at Hotel Sheraton Tahiti (http://www.sheratonsintahiti.com/tahiti/us) that is conveniently positioned between Faa'a international airport and Papeete.

Topics of interest include (but are not limited to):

- Architectures for Dependable Computer Systems
- Architectures and Protocols for Computer Security
- Dependability of High-Speed Networks
- Dependability Measurement, Modeling and Evaluation
- Dependability in VLSI
- E-commerce and Web services Dependability
- Fault Tolerance in Distributed & Real-Time Systems
- Fault Tolerance in Mobile Systems

- Fault Tolerance in Multimedia Systems
- Fault Tolerance in Transaction Processing
- Hardware and Software Testing, Verification and Validation
- Information Assurance, Survivability, and Intrusion Tolerance
- Internet Dependability and Quality of Service
- Safety-Critical Systems
- Software Reliability Engineering

Manuscripts should be submitted in the following categories: Regular Papers and Practical Experience Reports. Regular Papers should describe original research (not submitted or published elsewhere) and be not more than 20 double-spaced pages including figures and tables using 11-point type. Practical Experience Reports (of 5-12 pages) should describe an experience or a case study, such as the design and deployment of a system or actual failure and recovery field data. The title page should include a 150-word abstract, five keywords, authors' names and address and include a line specifying whether the submission is a Regular Paper or a Practical Experience Report. The full mailing address, phone, fax and email address of the correponding author should be specified. All submissions must be made electronically.

Additional submission opportunities are also possible at a later deadline under the form of Fast Abstracts.

Please visit our web site for full submission instructions and updated information on the symposium.

IMPORTANT DATES



Sponsored by IEEE Computer Society Technical Committee on Fault-Tolerant Computing and LAAS-CNRS In Cooperation with IFIP WG 10.4 on Dependable Computing and Fault Tolerance



