Free Ride for Resilience: from the Perspective of Software Engineering

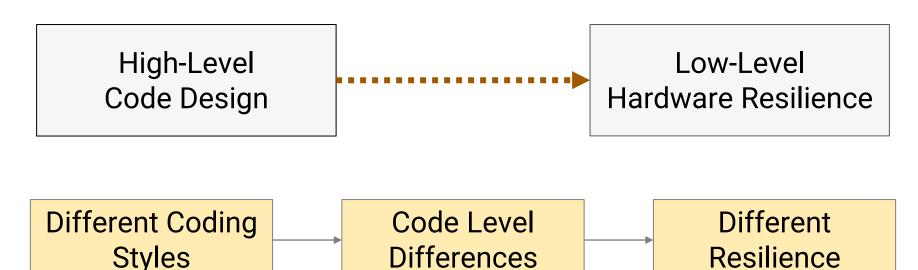
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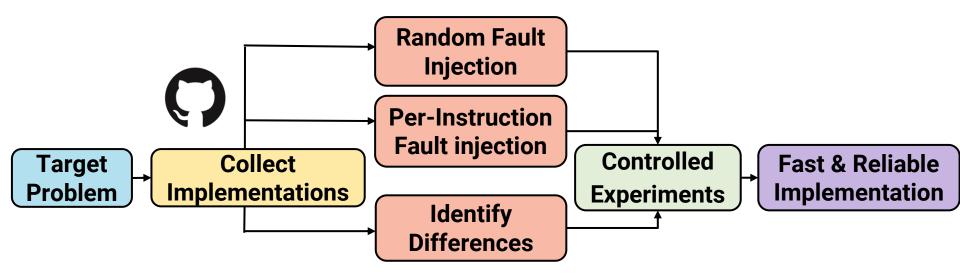




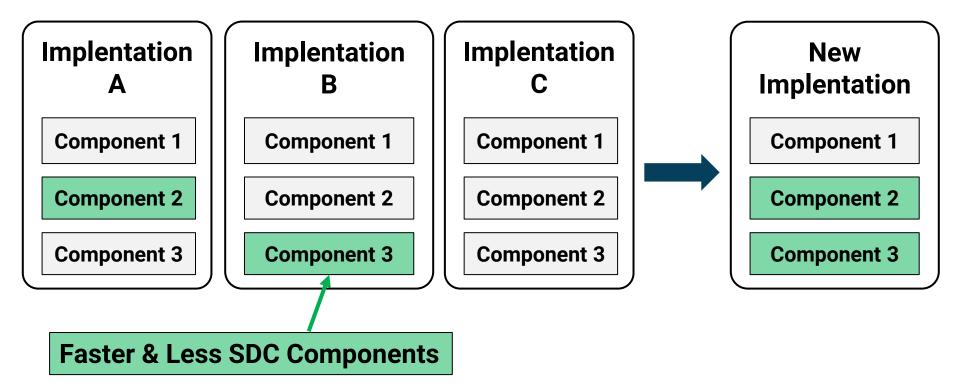


Free Ride: Low SDC Probability & High Performance?

Approach



Approach



Proof-of-Concept

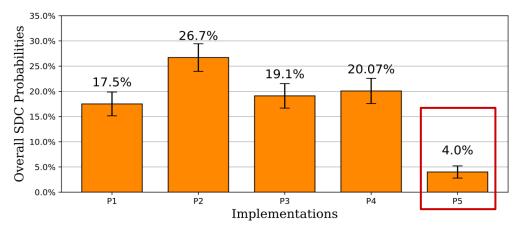
- A Debian server with two 20-core CPU
- Fault Model
 - Single bit-flip injections accurate
 - Errors in computation units/data path
 - One fault per program execution
 - Use LLFI for fault injection
- Application : BubbleSort
 - Outer loop
 - Inner loop
 - Swaption

Algo	rithm 1 Bubble Sort	
1: p	procedure BUBBLESORT(A)	
2:	$n \leftarrow A $	
3:	for $i \leftarrow 1$ to $n-1$ do	⊳ Outer loop
4:	for $j \leftarrow 0$ to $n - i - 1$ do	▷ Inner loop
5:	if $A_j > A_{j+1}$ then	▷ Swaption
6:	swap A_j and A_{j+1}	
7:	end if	
8:	end for	
9:	end for	
10: e	nd procedure	

Proof-of-Concept

• Overall SDC probabilities accros 5 programs

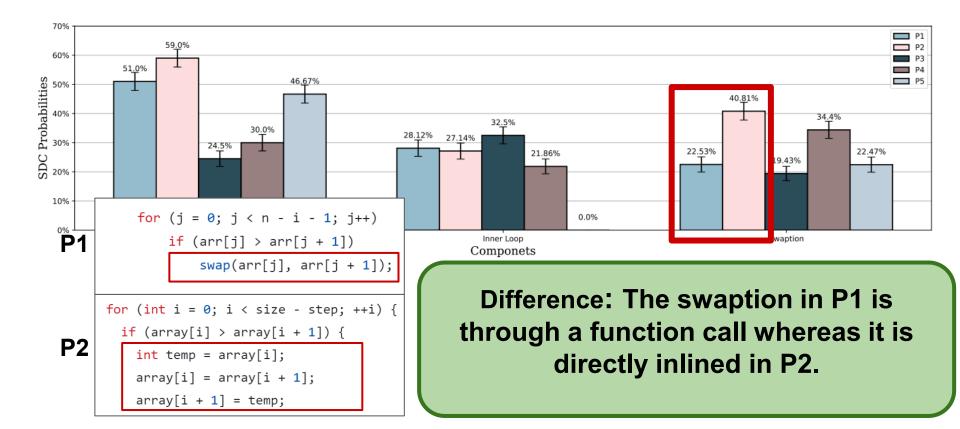
- The SDC probabilities range: 4% ~ 26.7%
- P5's DI count: much higher



NUMBER OF DYNAMIC INSTRUCTIONS OF 5 PROGRAMS.

Number of Dynamic Instructions	
5,889,058,005	
6,087,856,752	
5,685,594,374	
5,489,077,998	
9,088,698,022	

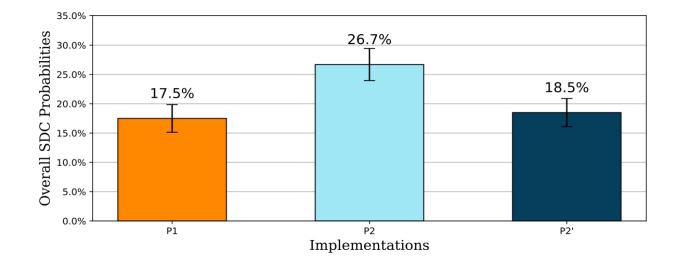
Proof-of-Concept



Result

• Controlled experiment of Hypothesis

- P1 swaption replaced with P1 implementation to generate P2'
- The SDC probability of P2' is only 18.5%



Future Works

• Future Work

- Generalize methodology
- Different performance measures and tradeoffs
- Create comprehensive guidance for reliable coding
- Github & LeetCode problems
- Course Projects
- Human studies