Research Vision

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CritiX Lab (Critical and Extreme Security and Dependability)

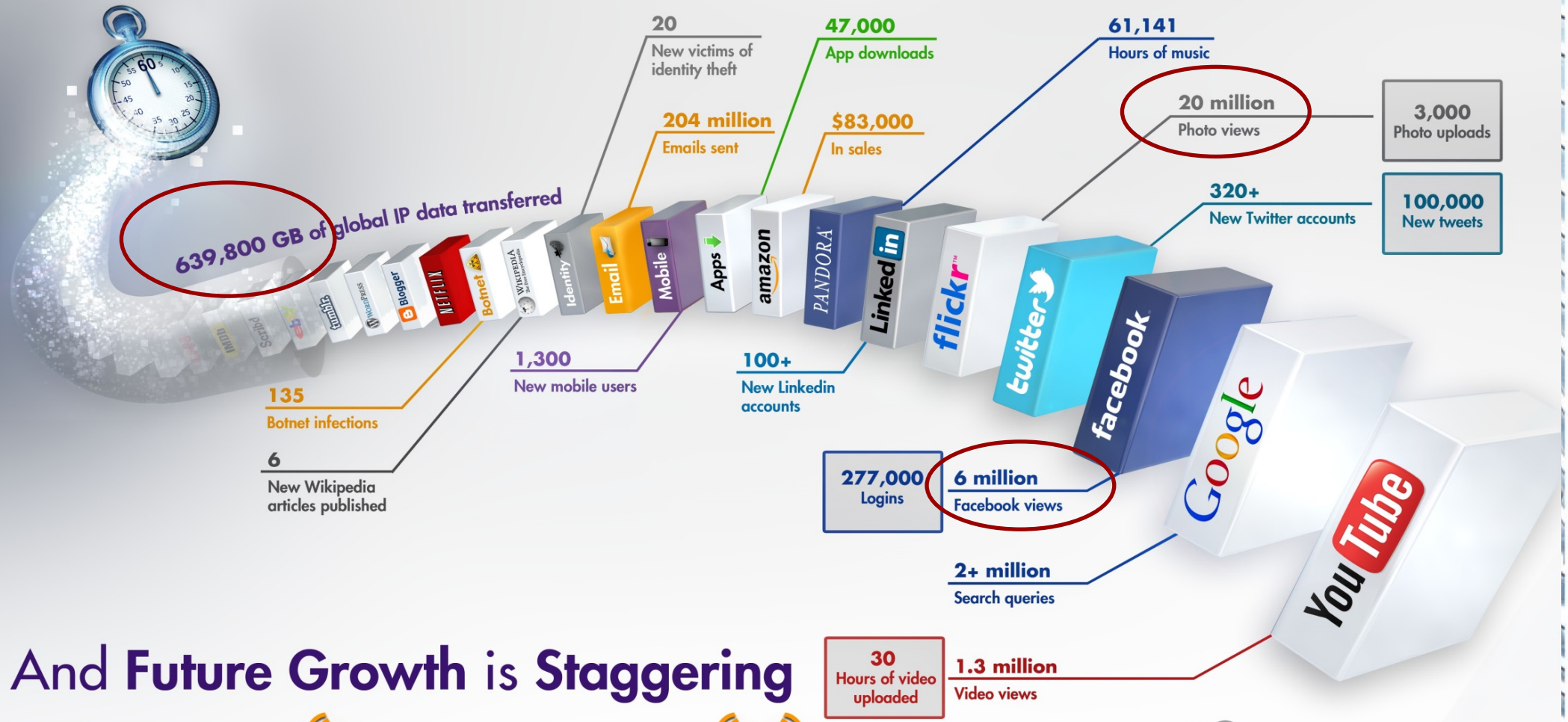
Presentation to .

a.k.a. INFRASTRUCTURE

Is the world becoming net-centric?

Let's dare a vision of the near future

What Happens in an **Internet Minute?**



And **Future Growth** is **Staggering**

*www.intel.com/.../**internet-minute**-infographic.html*

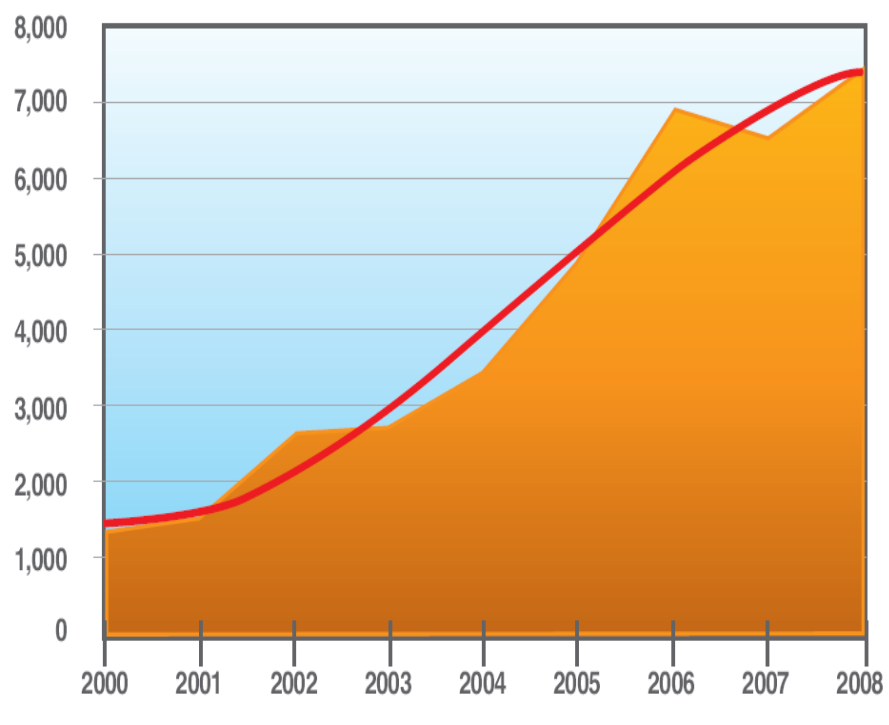
A world full of threats?

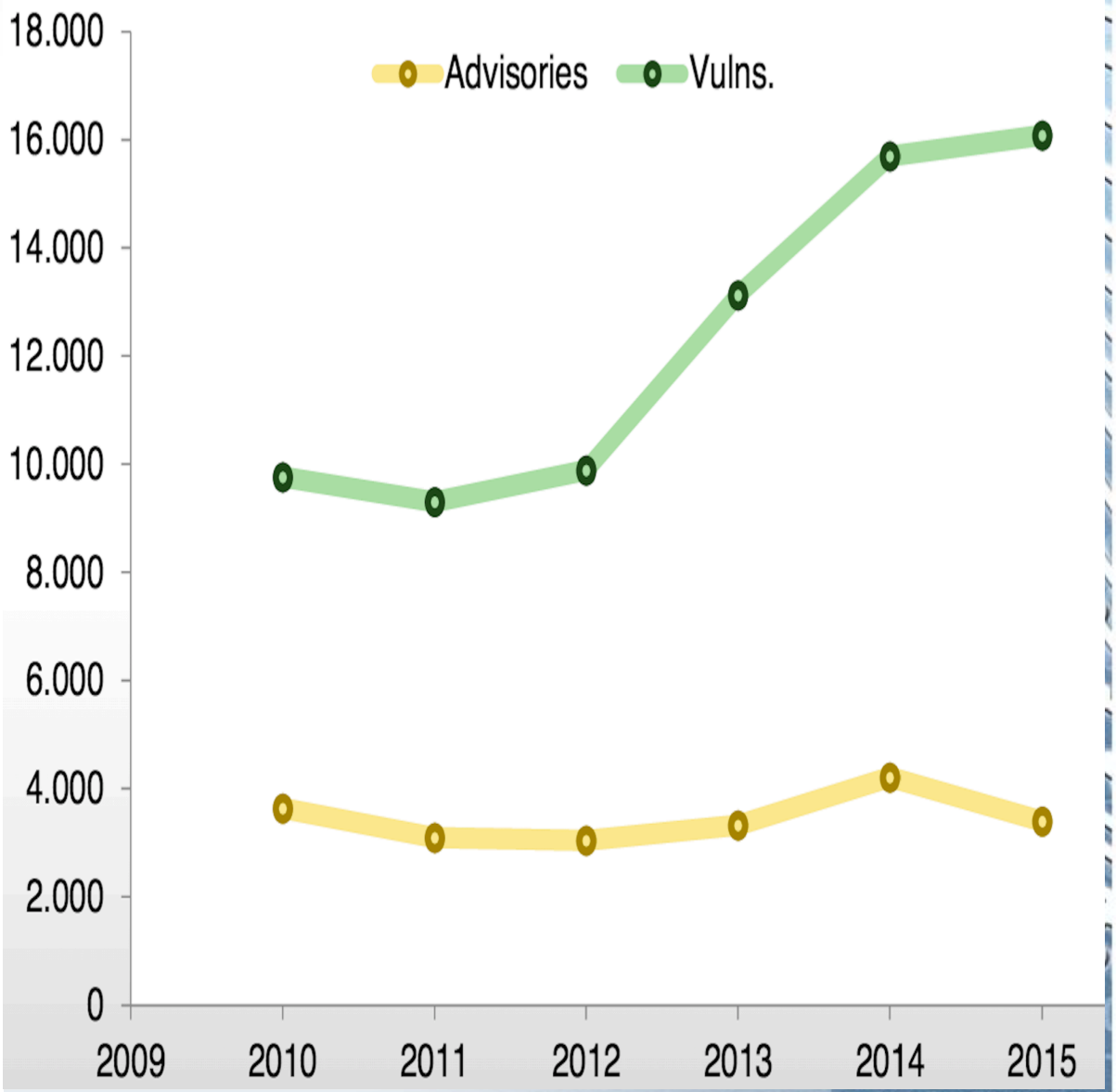
- targeted attacks and advanced persistent threats (PRISM, TAO, APT1, etc.)
- weakening and subversion of comms and computing services
- threats to privacy: mass surveillance and data collection
- sophisticated automated cyber weapons (Stuxnet, Flame, etc.)
- organised crime (RBN, etc.)

Conventional Software Vulnerabilities

ever increasing

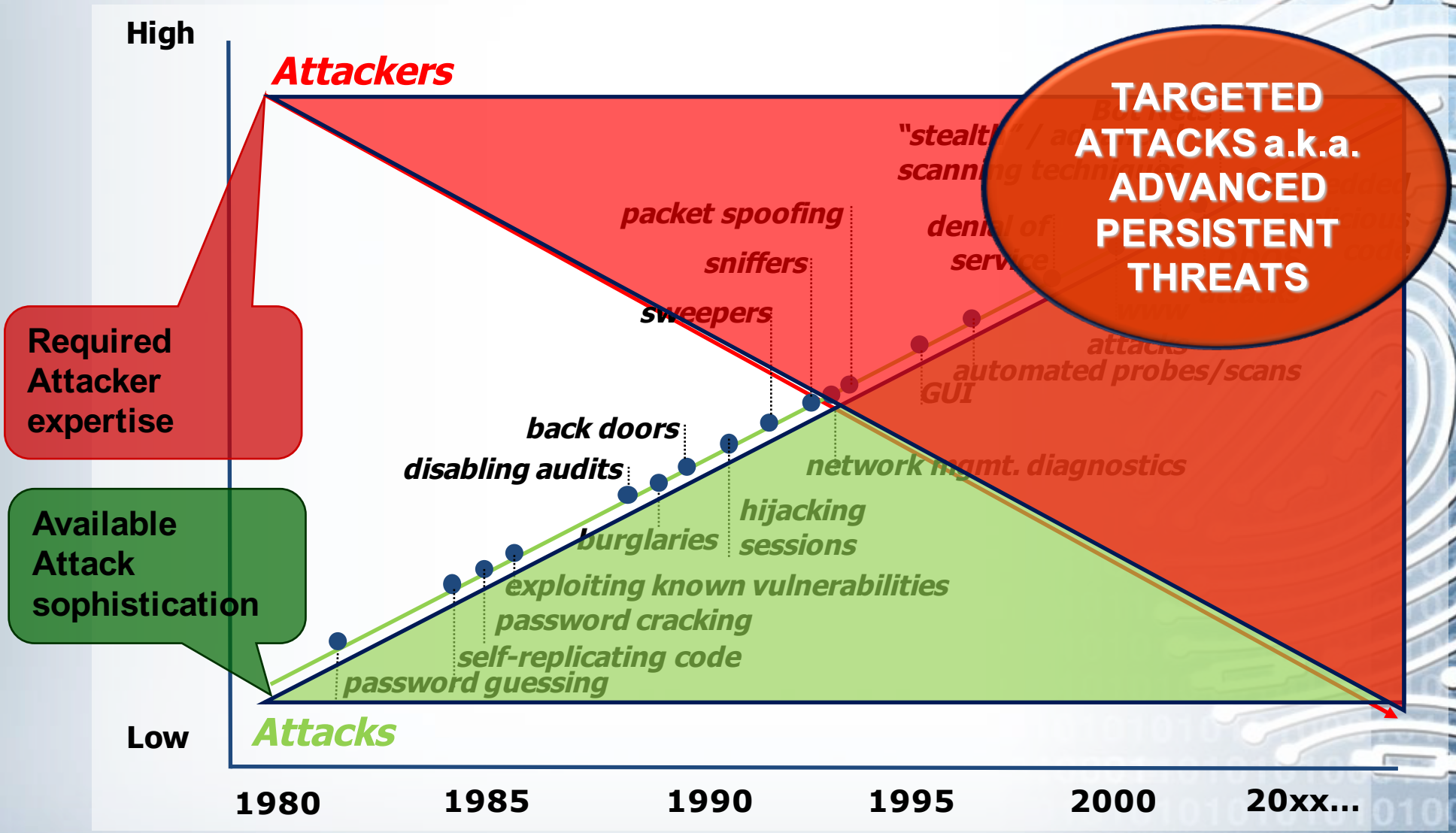
Number of new vulnerabilities per year





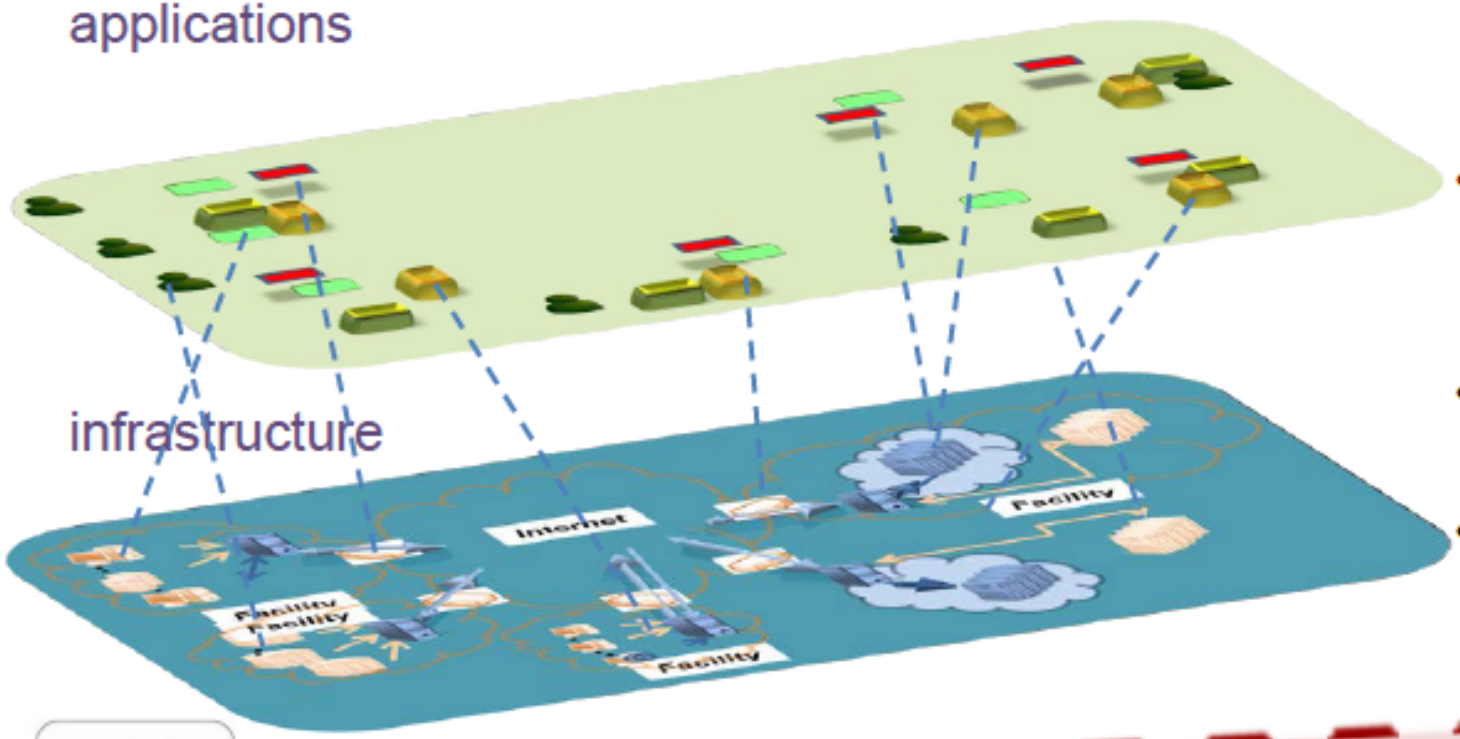
(Sources: IBM xForce, Symantec, Telexa)

Attack sophistication vs. attacker expertise



(Source: Adapted from Lipson, H. F., Tracking and Tracing Cyber-Attacks: Technical Challenges and Global Policy Issues, Special Report CMS/SEI-2002-SR-009, November 2002. (CERT)

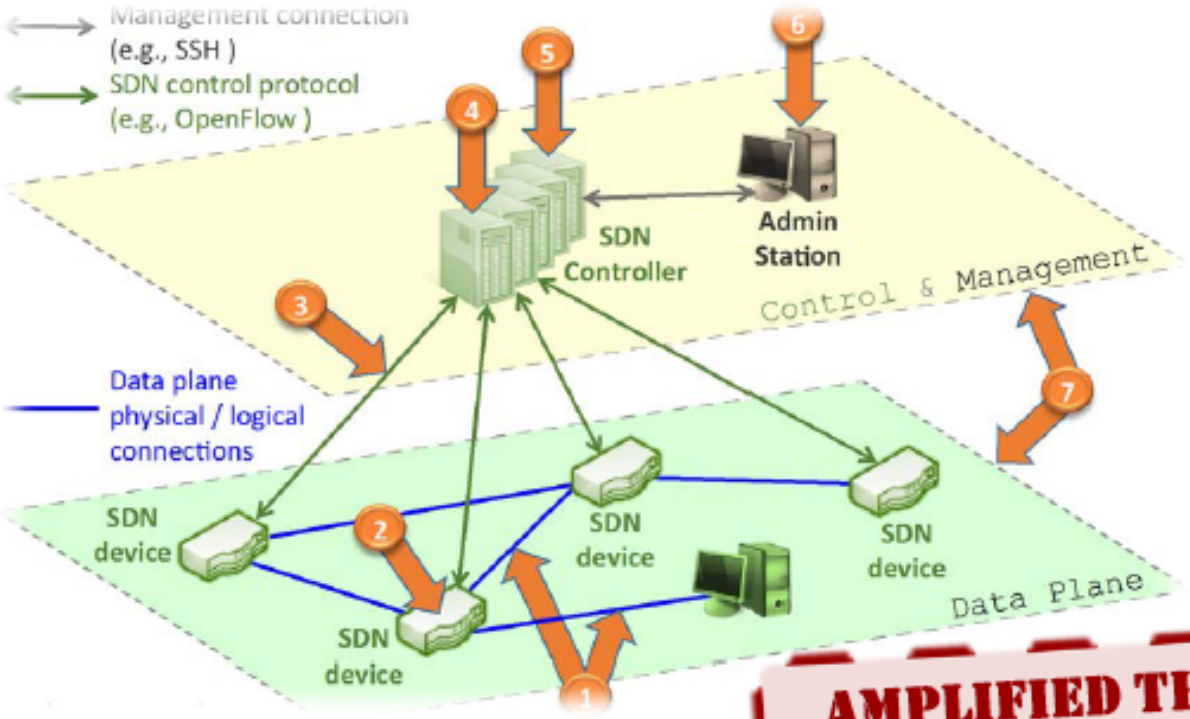
platforms and applications



- short-term and dynamic dev/test/deploy cycle
- weak separation between both layers
- long-term and stable dev/test/deploy cycle
- good Sec&Dep

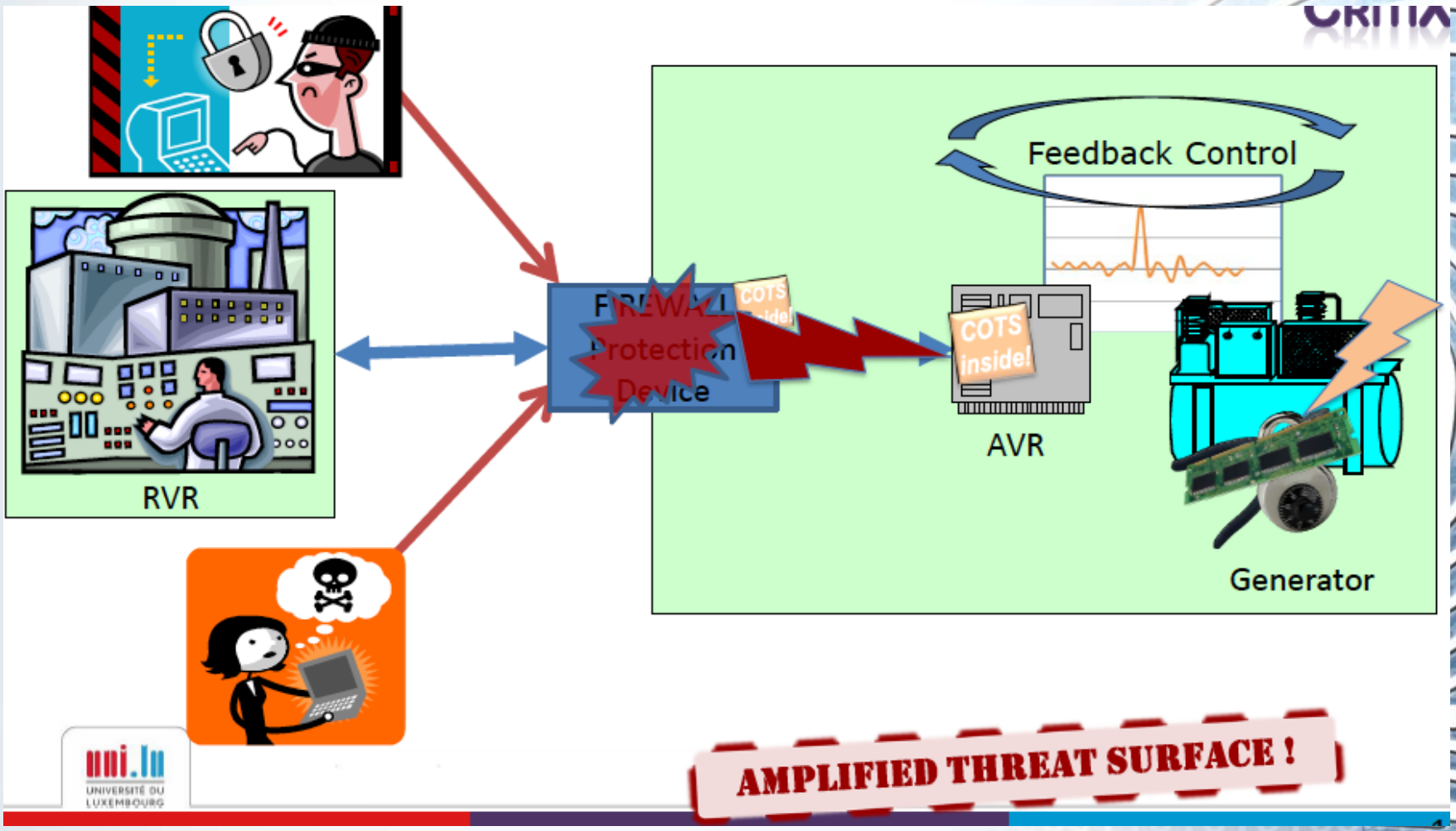
AMPLIFIED THREAT SURFACE !

- ironically, causes of concern lie in SDN's main benefits:
  - *network programmability and control logic centralization*
  - *smaller diversity*
  - *new threats that did not exist before or were harder to exploit*



[Kreutz et al., HotSDN'13]

AMPLIFIED THREAT SURFACE !


Feedback Control
FIREWALL
Protection
Device
COTS
inside
COTS
inside
AVR
RVR
Generator
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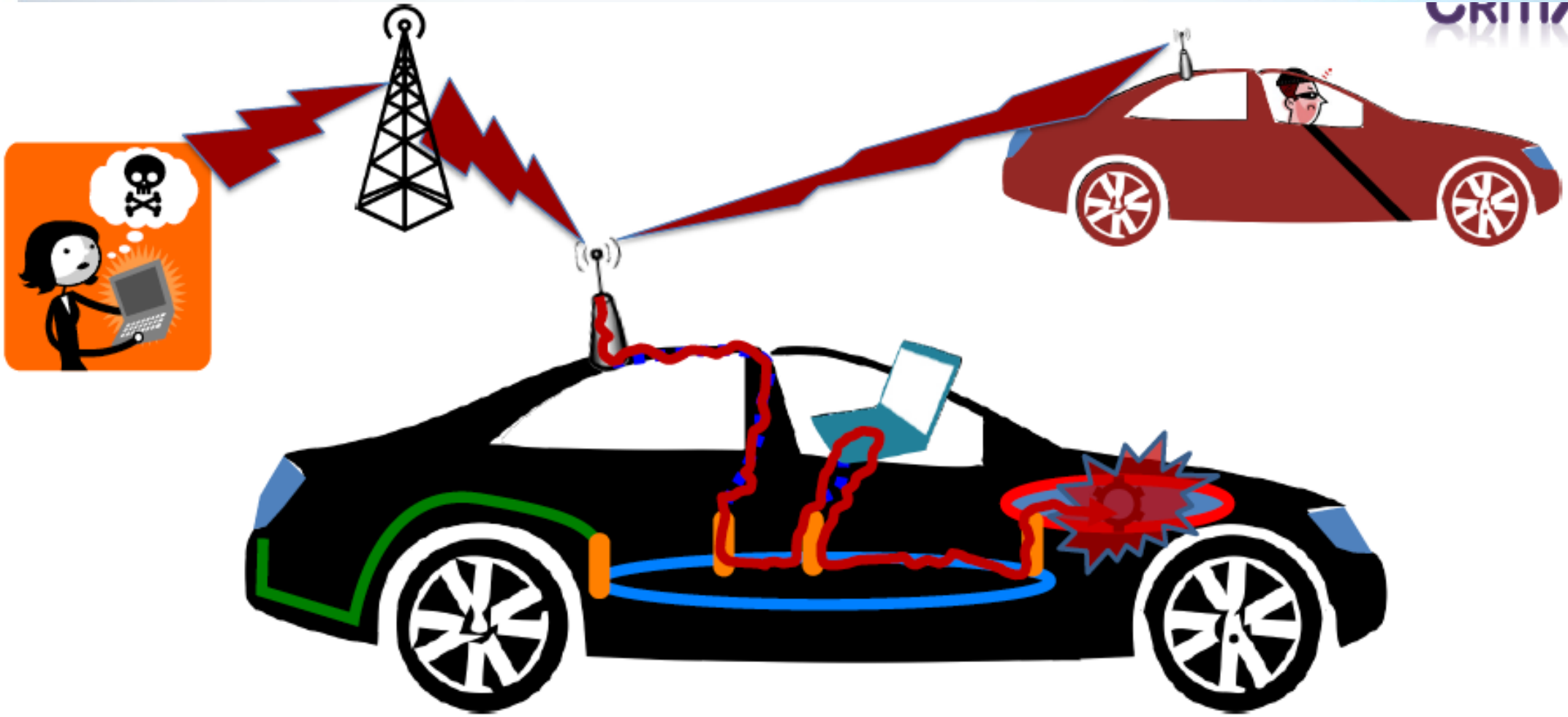
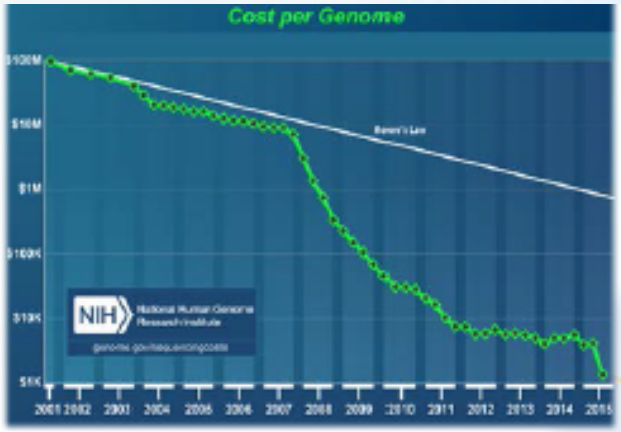


AMPLIFIED THREAT SURFACE !

- Due to Next Generation Sequencing, we will soon be generating more genomic data than we can currently securely store and process



- Handling terabytes of information has become the norm for genomics, but trend is to scale up.

- This brings cloud and big data analytics onto the agenda, challenging the secure storage and analysis of such data, e.g. use of clouds

AMPLIFIED THREAT SURFACE !

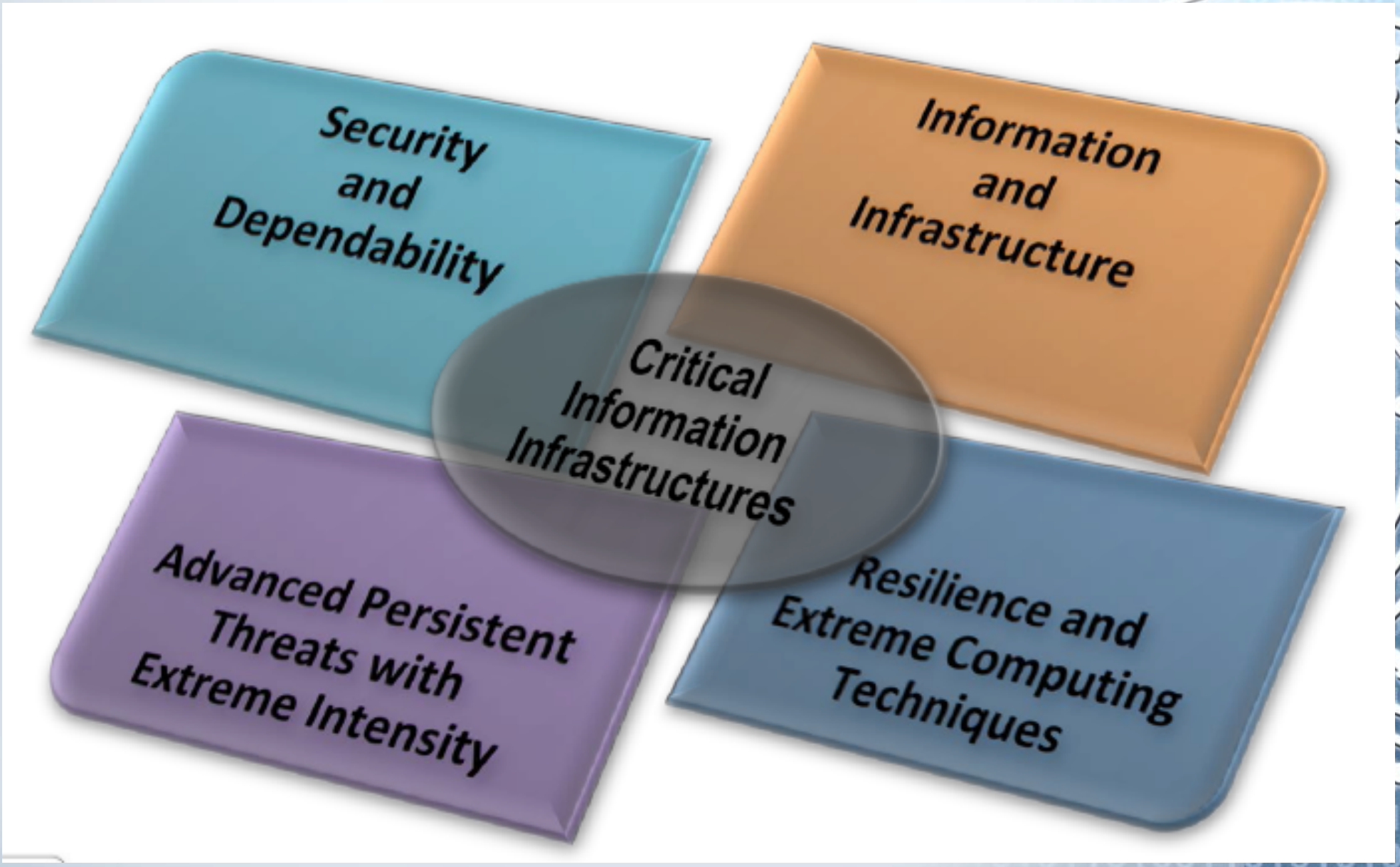
CritiX research vision

CRITIX - Critical and Extreme Security and Dependability Research Lab

Research enablers for the next generation of protection

- *Critical Security and Dependability:*
  - information infrastructures under advanced persistent threats
- *Extreme Computing:*
  - CSE pushed to the extremes of functional and non-functional properties
- *Architecting and designing for resilience:*
  - accidental and malicious faults; protection in an incremental way; automatic adaptation

Meeting the Challenges of Critical Dependability and Security



Architecting and designing for resilience

- comprehensive approach to those threats, from first principles: *"build defence in"*
- simultaneously coping with accidental and malicious faults
- provide protection in an incremental way
- automatically adapt to a dynamic range of severity of threats
- seek unattended and perpetual operation

Is resilience really necessary?

Is resilience really necessary?

- Adm. Michael Rogers, NSA Director and commander of US Cyber Command, said that the question "How, in the midst of degradation and penetration, can we still have confidence in the systems?" is better served by ***focusing on resilience rather than on prevention***.
- [Editor's Note]: This is the new theme for cybersecurity - the ability to ***continue fighting when you're hurt*** is the differentiator between a successful security organization and the one picking up the pieces after an incident and wondering what happened.

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IT security shifts from prevention to resiliency

Sep. 22, 2014 - 06:00AM | By AARON BOYD | Comments

Recommend 31

Adm. Michael Rogers: Preventing or stopping intruders is only half of the equation for maintaining resiliency. (Mark Wilson/Getty Images)

The discussion on cybersecurity has shifted as CIOs and CTOs come to the realization that no system is immune to attacks and breaches. The conversation is now about "cyber resiliency."

"How, in the midst of degradation and penetration, can we still have confidence in the systems?" Adm. Michael Rogers, NSA director and commander of U.S. Cyber Command, asked at the Billington Cybersecurity Summit in Washington. "Most organizations have tended to put their resources and focus on stopping people from penetrating their systems. I tell organizations that we have got to not only focus on stopping people... but how are you going to operate and remediate at the same time. That's resiliency."

Designing and architecting for resilience

1.	we want systems to operate through faults and attacks in a seamless manner, in an automatic way	Preventing and Tolerating Faults and Intrusions
2.	we want systems to endure the fact that operating conditions and environments are everyday more uncertain and/or hostile	Handling Incremental Threat Severity
3.	we want systems to be deployed in unattended manner	Resisting Continued Threats
4.	we want systems to attain very high levels of assurance	Validating and Assessing Assumptions and Mechanisms

(P. Verissimo, M. Correia, N. Neves, P. Sousa, "Intrusion-Resilient Middleware Design and Validation", in Information Assurance, Security and Privacy Services, ser. Handbooks in Info Systems. Emerald, May'09)

A methodic approach to modular and distributed resilient computing

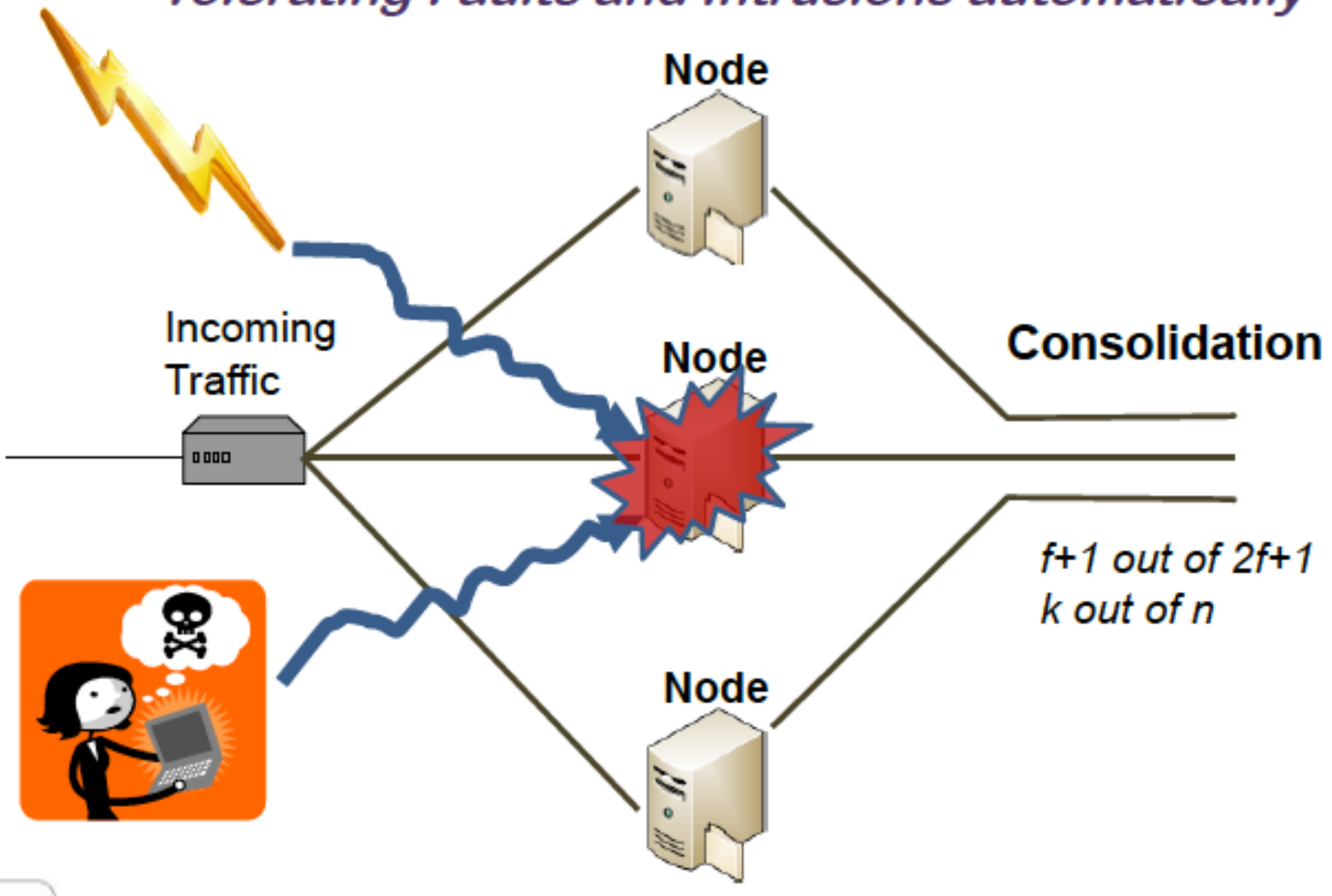
- Fault and intrusion tolerance, or automatic security and dependability
- Handle increasing threat severity
- Resist continued threats

- Divide-and-conquer to beat extreme threats
- Hybrid models and architectures
- Ultra-reliable trusted components
- High-confidence vertical verification
- Privacy- and integrity-preserving data processing

Fault and Intrusion Tolerance (FIT)

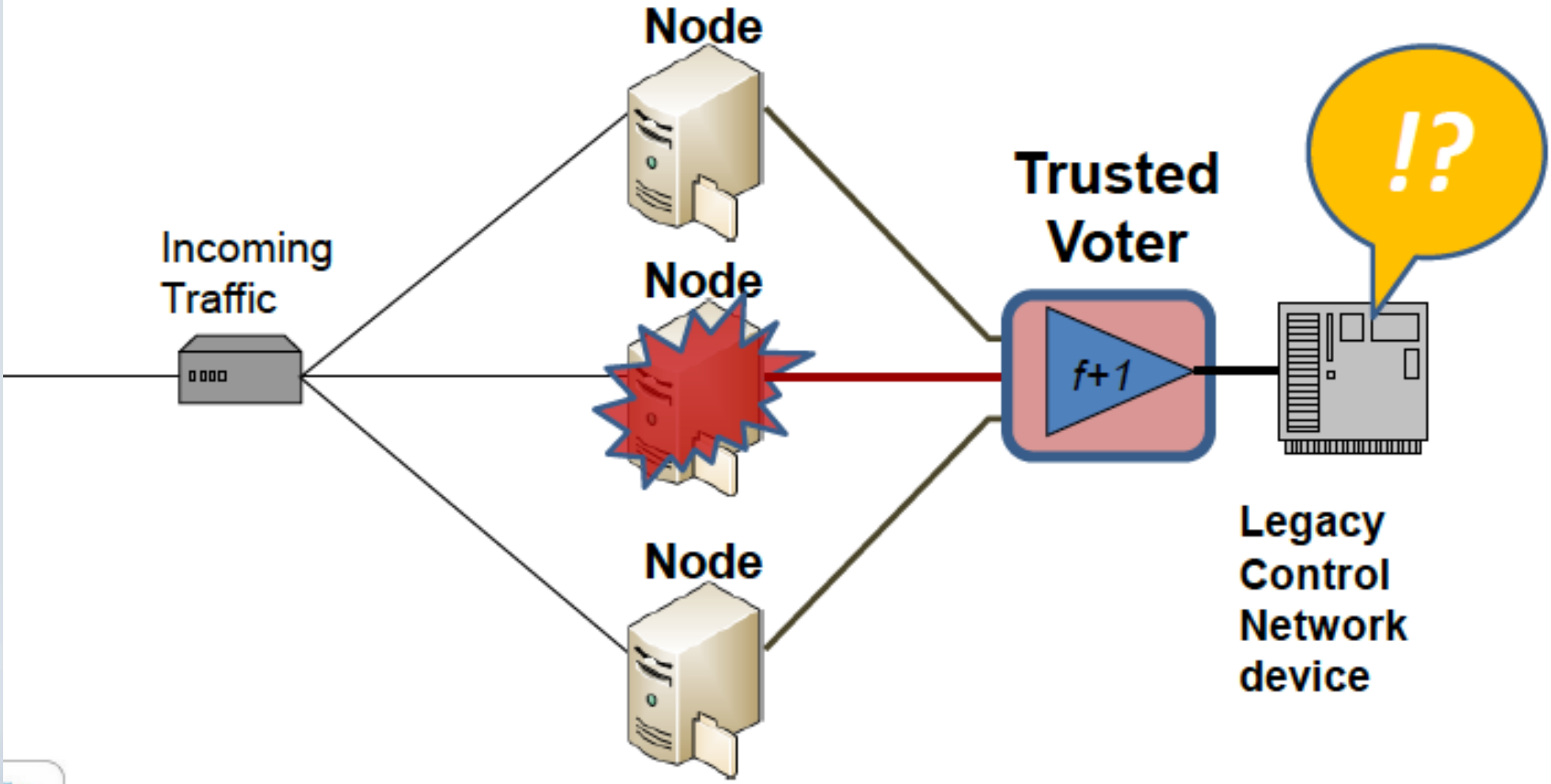
An abstract solution

Tolerating Faults and Intrusions automatically



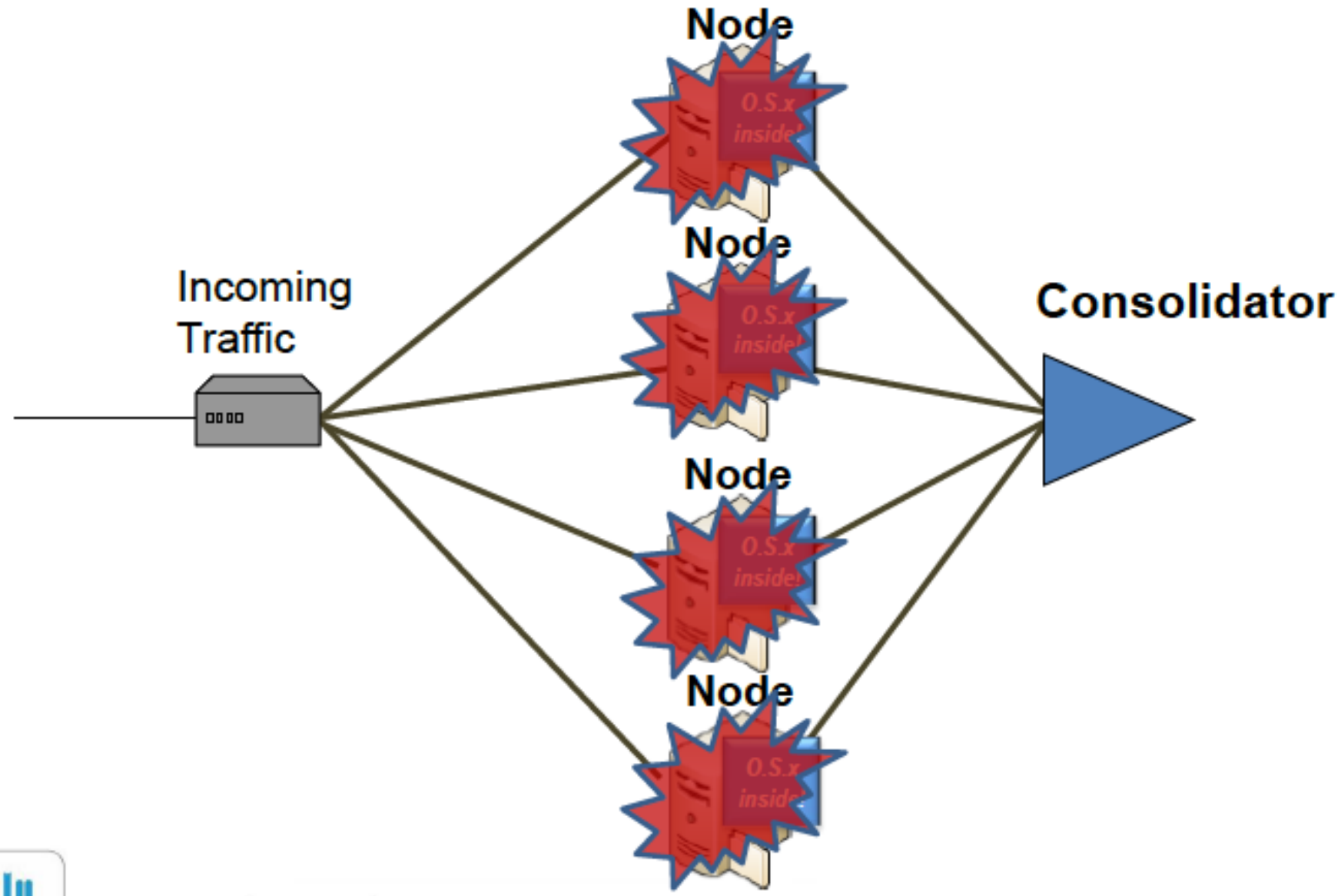
Fault and Intrusion Tolerance (FIT)

Handling legacy systems



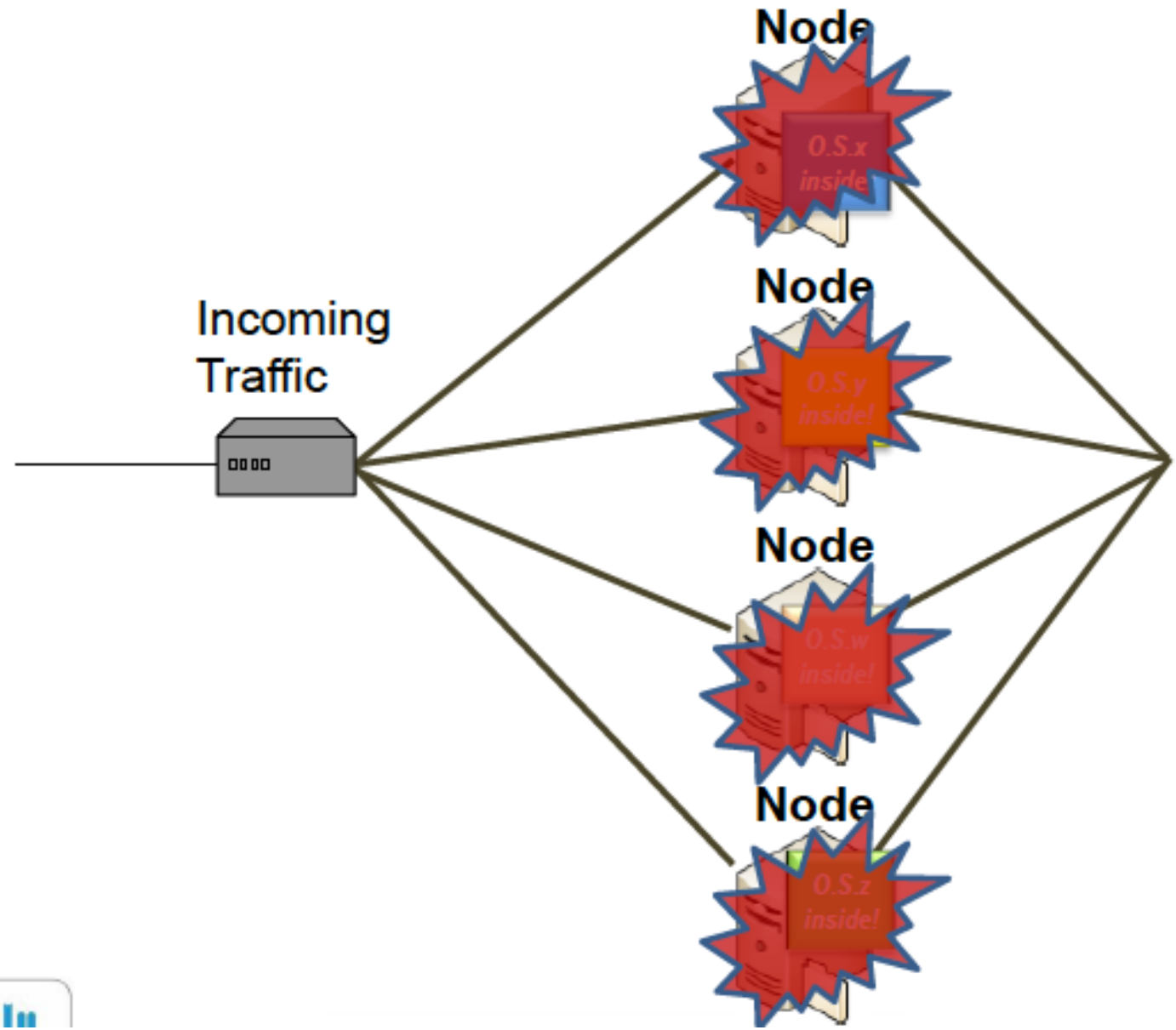
Fault and Intrusion Tolerance (FIT)

The common-mode fault problem



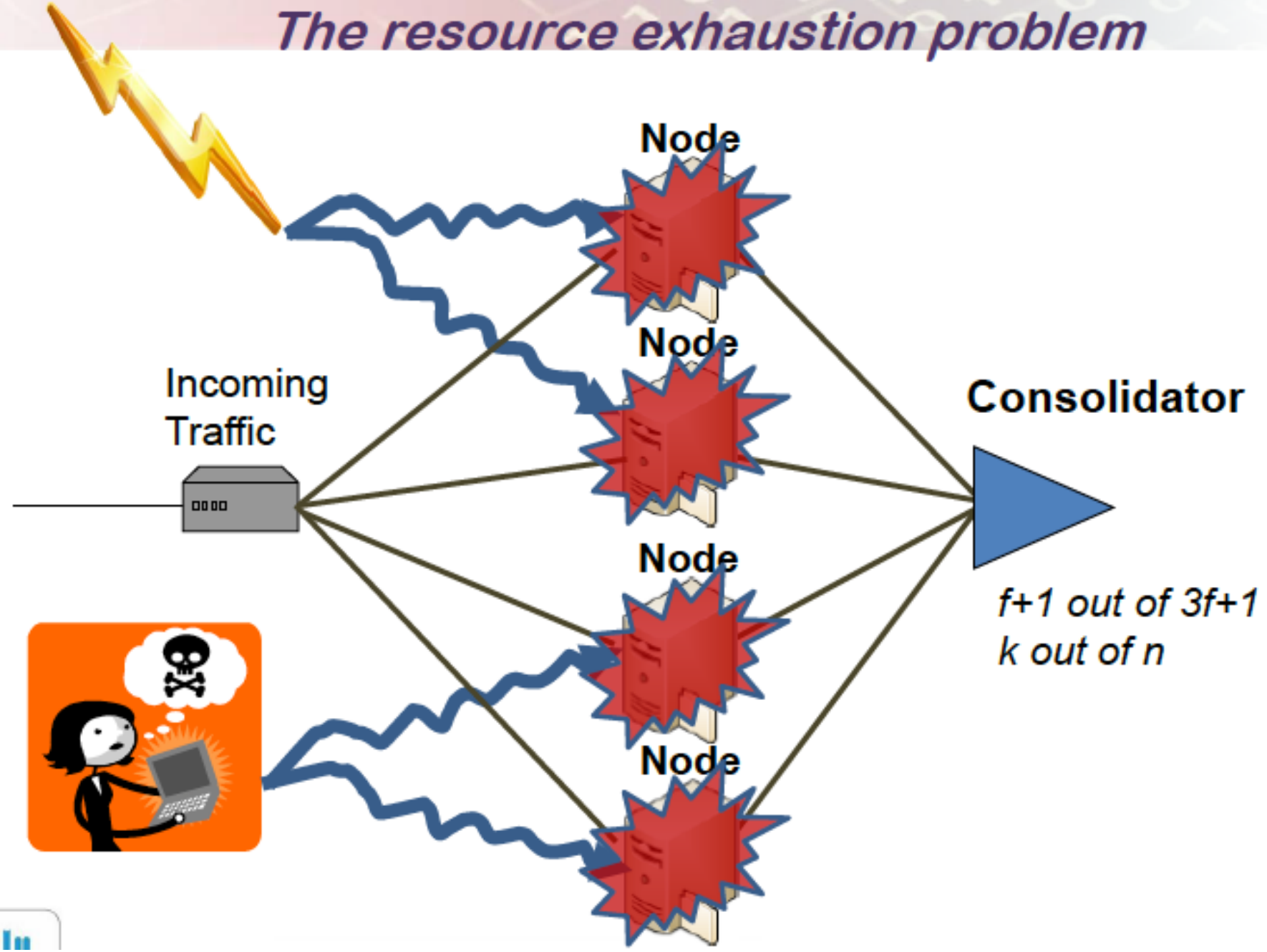
Fault and Intrusion Tolerance (FIT)

Mitigating common-mode faults



Fault and Intrusion Tolerance (FIT)

The resource exhaustion problem



Fault and Intrusion Tolerance (FIT)

Resisting Continued Threats

Seeking (unattended) perpetual execution

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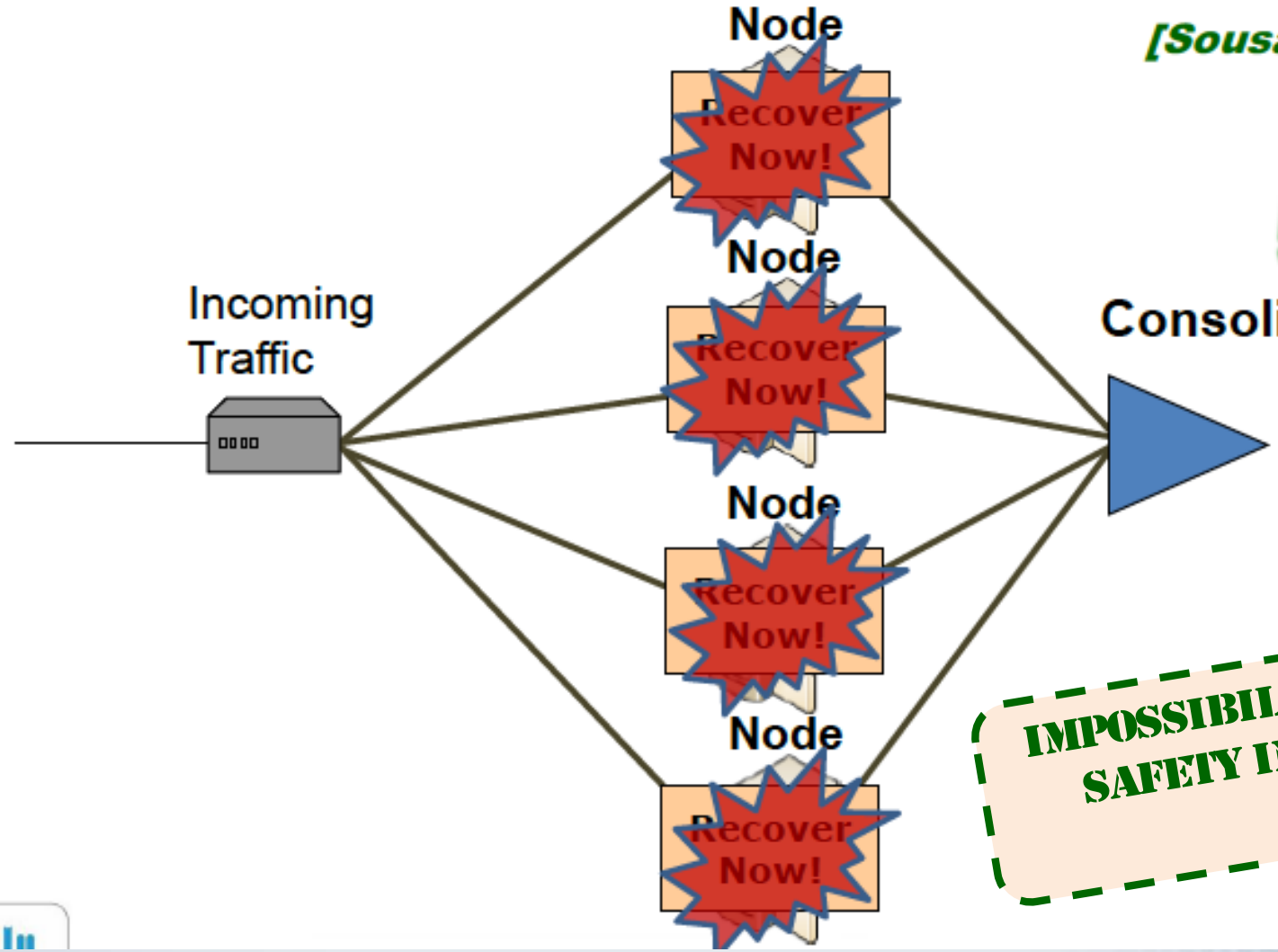
[Sousa et al., DSN 05]



EXHAUSTION SAFETY!

IMPOSSIBILITY OF EXHAUSTION SAFETY IN ASYNCHRONOUS SYSTEMS

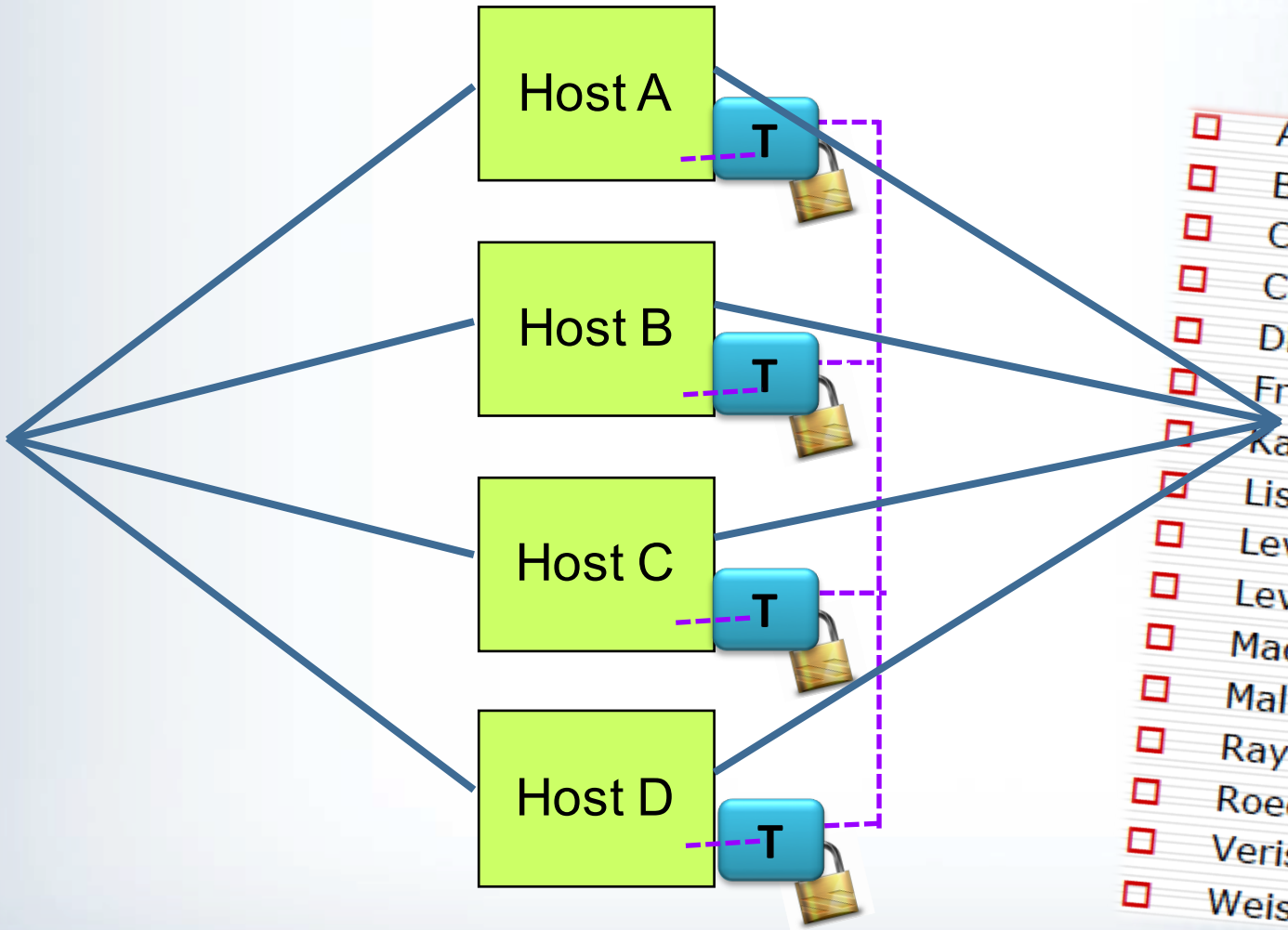
A methodic approach to modular and distributed resilient computing

- Fault and intrusion tolerance, or automatic security and dependability
- Handle increasing threat severity
- Resist continued threats

- Divide-and-conquer to beat extreme threats
- Hybrid models and architectures
- Ultra-reliable trusted components
- High-confidence vertical verification
- Privacy- and integrity-preserving data processing

Hybrid models and architectures

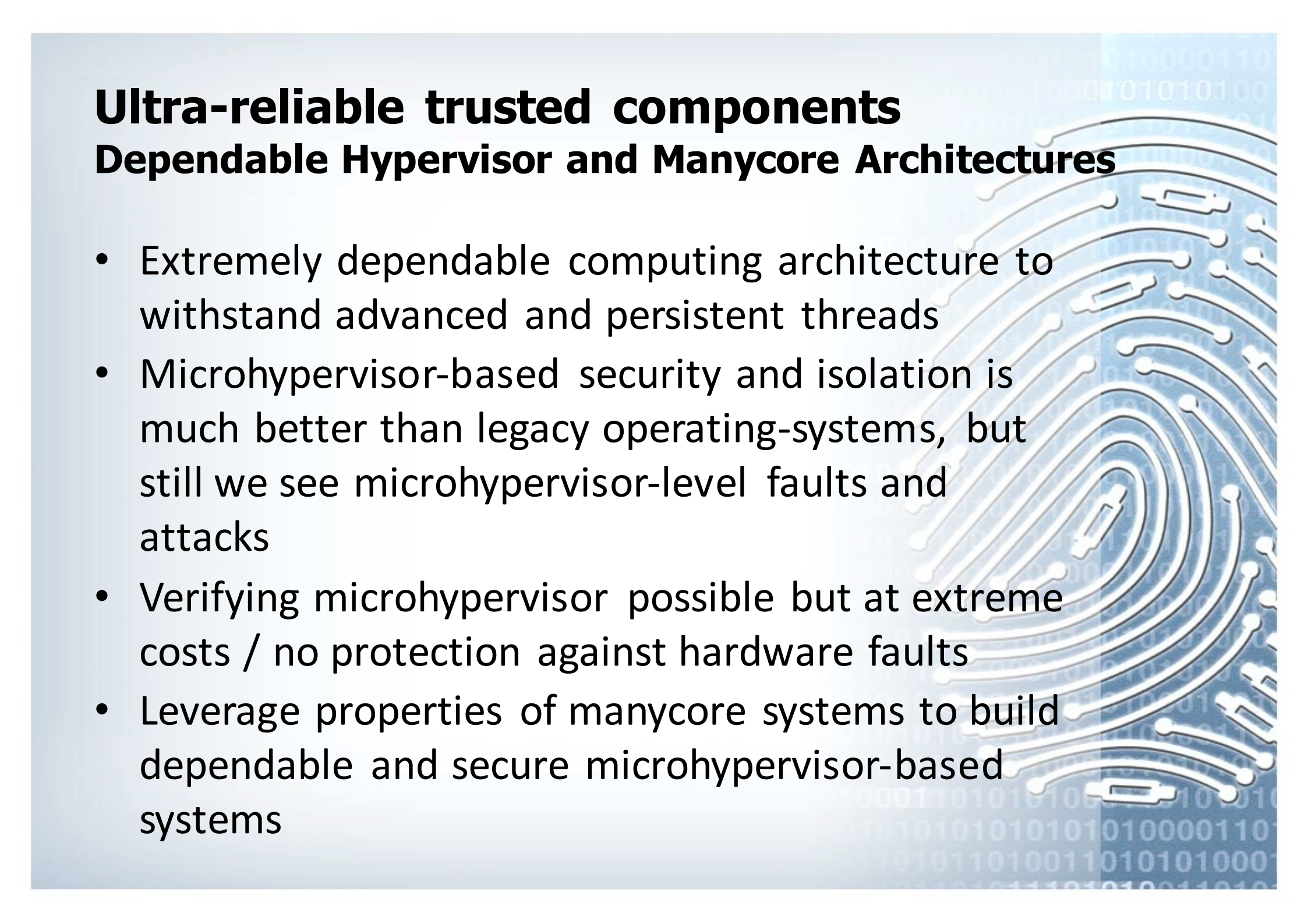
Leveraging power at right place right time



- Aguilera et al.
- Baldoni et al.
- Chun, Maniatis, Kubiatowicz et al.
- Clement, Junqueira, et al.
- Distler, Kapitza, Reiser et al.
- Friedman et al.
- Kapitza, Cachin et al.
- Liskov, Castro
- Levin, Douceur et al.
- Levitt, Duan et al.
- Macedo, Gorender, Raynal
- Malkhi et al.
- Raynal et al.
- Roeder, Schneider
- Verissimo et al.
- Weisnberg, Dolev et al.

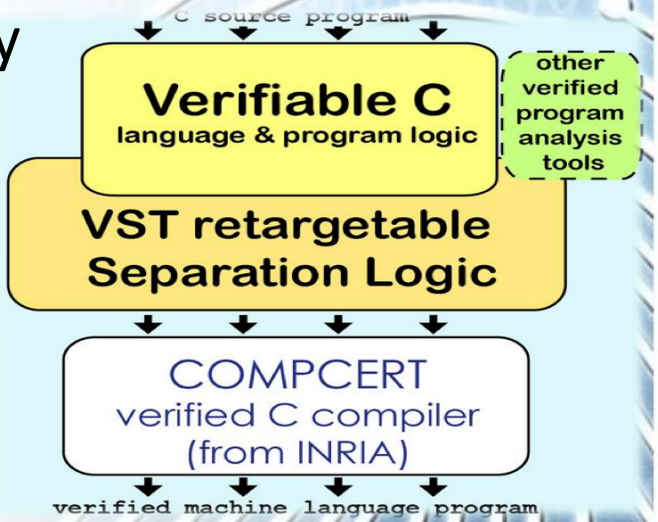
Ultra-reliable trusted components
Dependable Hypervisor and Manycore Architectures

- Extremely dependable computing architecture to withstand advanced and persistent threads
- Microhypervisor-based security and isolation is much better than legacy operating-systems, but still we see microhypervisor-level faults and attacks
- Verifying microhypervisor possible but at extreme costs / no protection against hardware faults
- Leverage properties of manycore systems to build dependable and secure microhypervisor-based systems



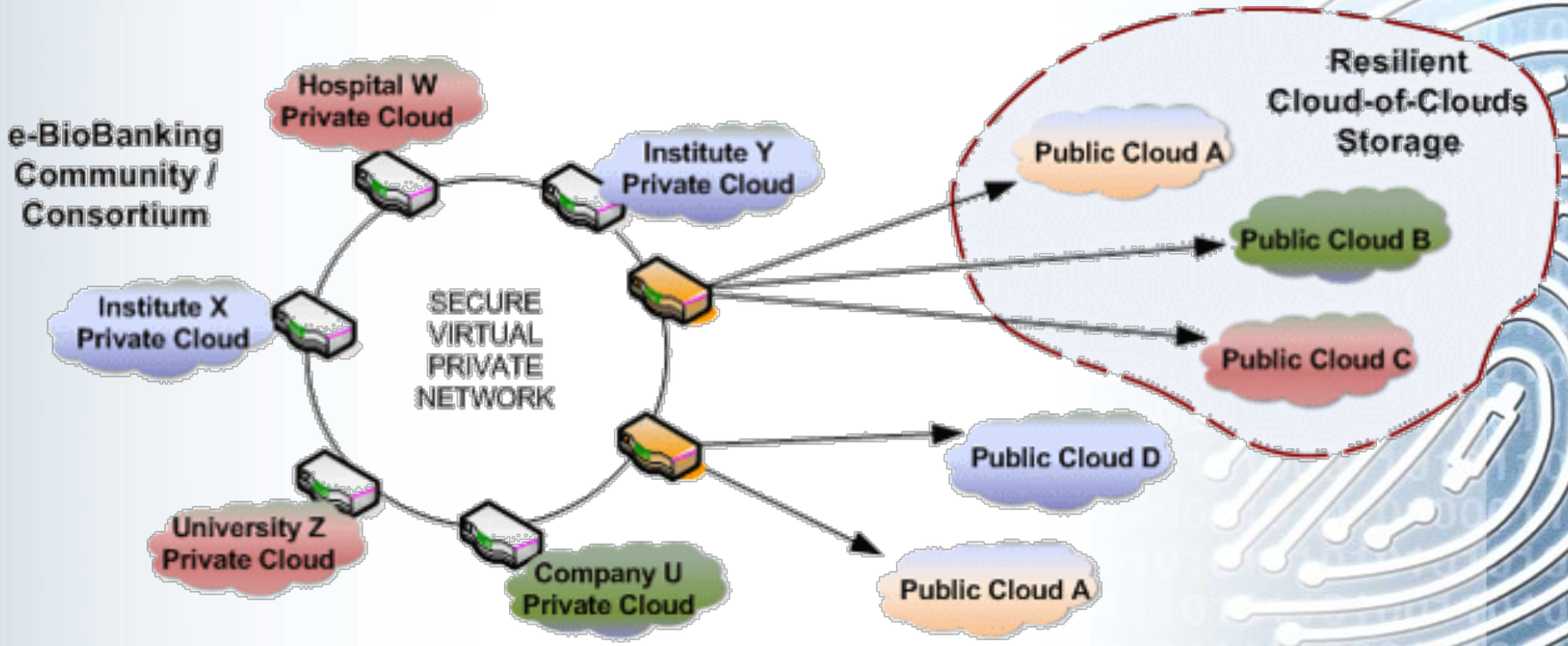
High-confidence vertical formal verification of critical components

- Verification of MinBFT's core trusted-trustworthy component (USIG):
  - C implementation of USIG
  - Coq specification/implementation for verification
  - Verify that C code satisfies Coq, through VST (Verified Software Toolchain)
  - Verify Coq spec satisfies desired safety properties through Coq
  - generate target code from C with CompCert (formally verified C compiler)



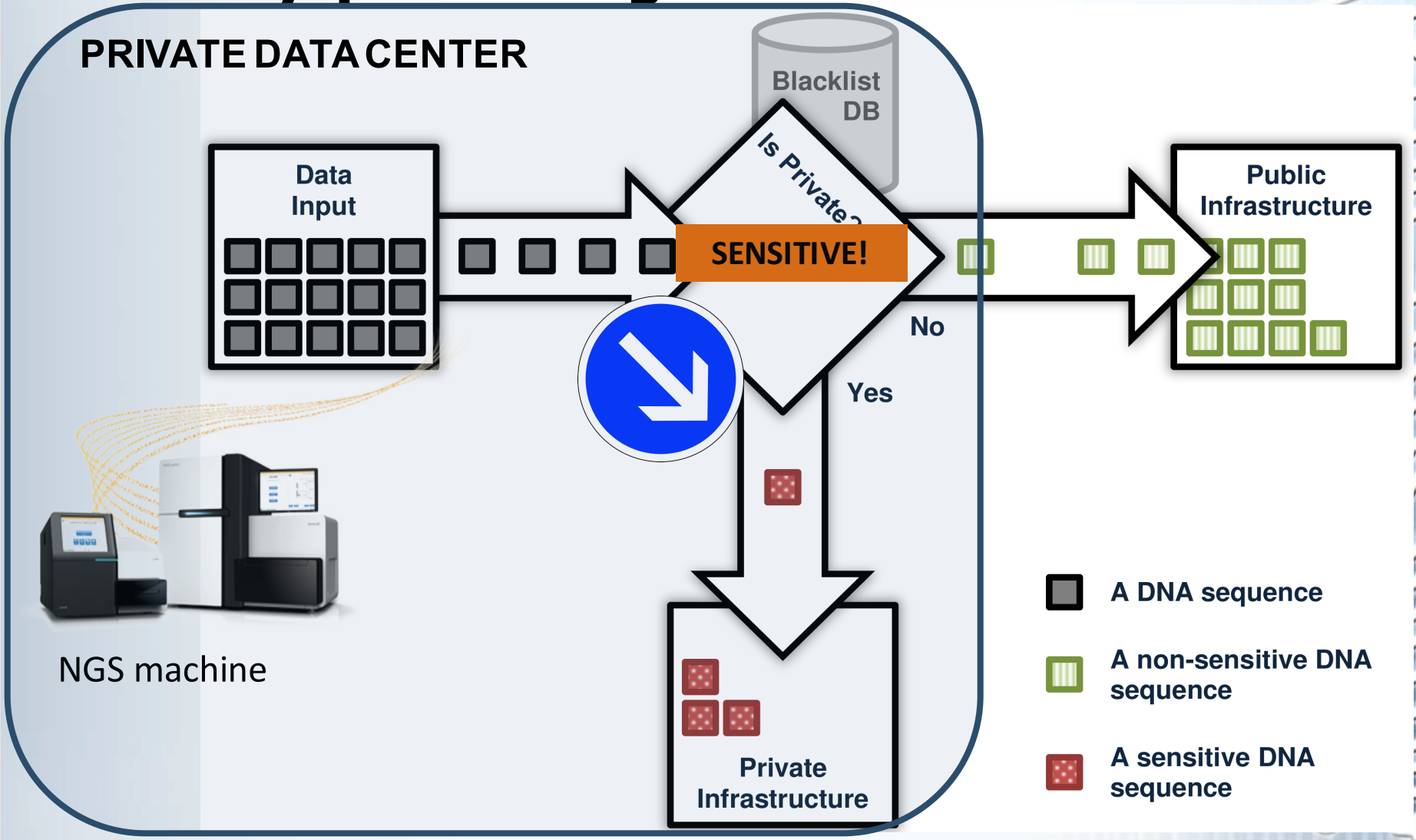
- Re-design and partial verification of BFT-SMaRt
  - One of the few fully-implemented and efficient BFT-SMR protocols
  - We plan on building trustworthy leader change and reconfiguration components to plug into BFT-SMaRt
  - As mentioned above we will verify these components in Coq (theorem prover from INRIA)

Privacy- and integrity-preserving data processing
The e-biobanking vision



Alysson Bessani et al., BiobankCloud: a Platform for the Secure Storage, Sharing, and Processing of Large Biomedical Data Sets", in *Proc's of the 1st Int. Workshop on Data Mgt. and Analytics for Medicine and Healthcare (DMAH 2015)*, Hawaii, US, Sept. 2015.

Data privacy and integrity: Privacy-preserving Disclosure Filter

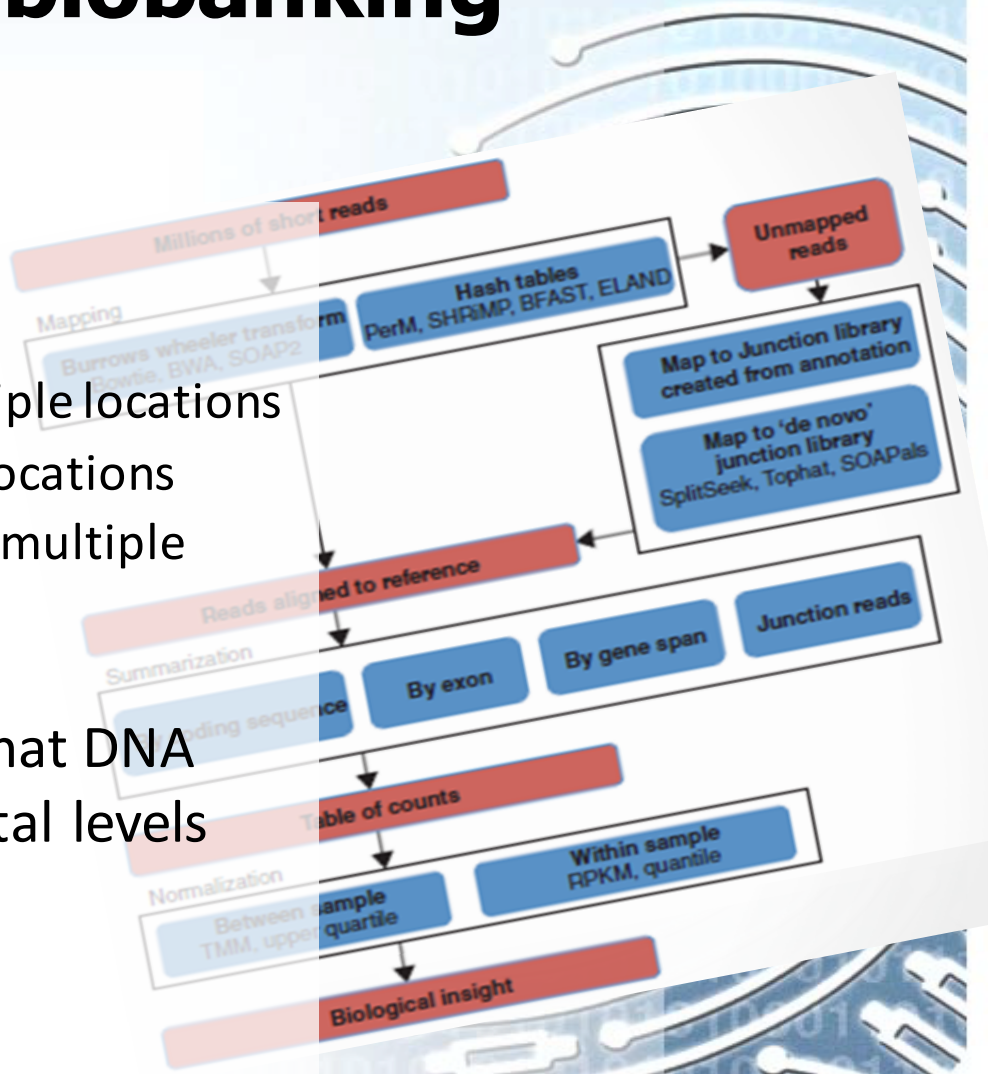


V. Cogo, A. Bessani, F. Couto, P. Verissimo, High-Throughput Method to Detect Privacy-Sensitive Human Genomic Data, in *Proc's of the Workshop on Privacy in the Electronic Society (WPES 2015)*, Denver, CO, US, Oct. 2015.

DNA workflows in an e-biobanking ecosystem

- Classical DNA workflow does not fit the e-biobanking vision, where:
  - data can be generated and stored in multiple locations
  - data should be accessible from different locations
  - data may be processed simultaneously at multiple locations, and/or remotely

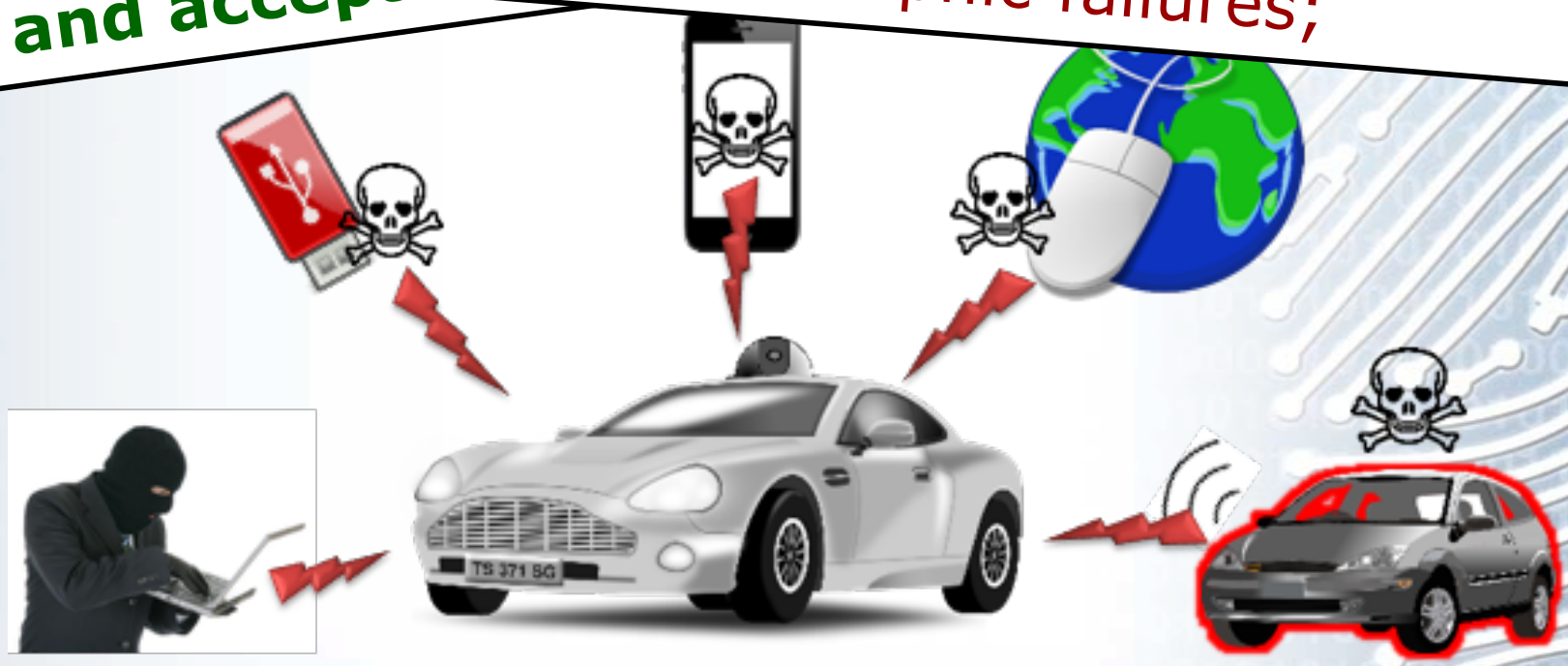
- (any) DNA workflows must guarantee that DNA data must be protected, with incremental levels according to need:
  - when it is stored,
  - when computations are executed on it,
  - and when the results of algorithms are published.



Privacy-preserving (really) distributed DNA alignment!

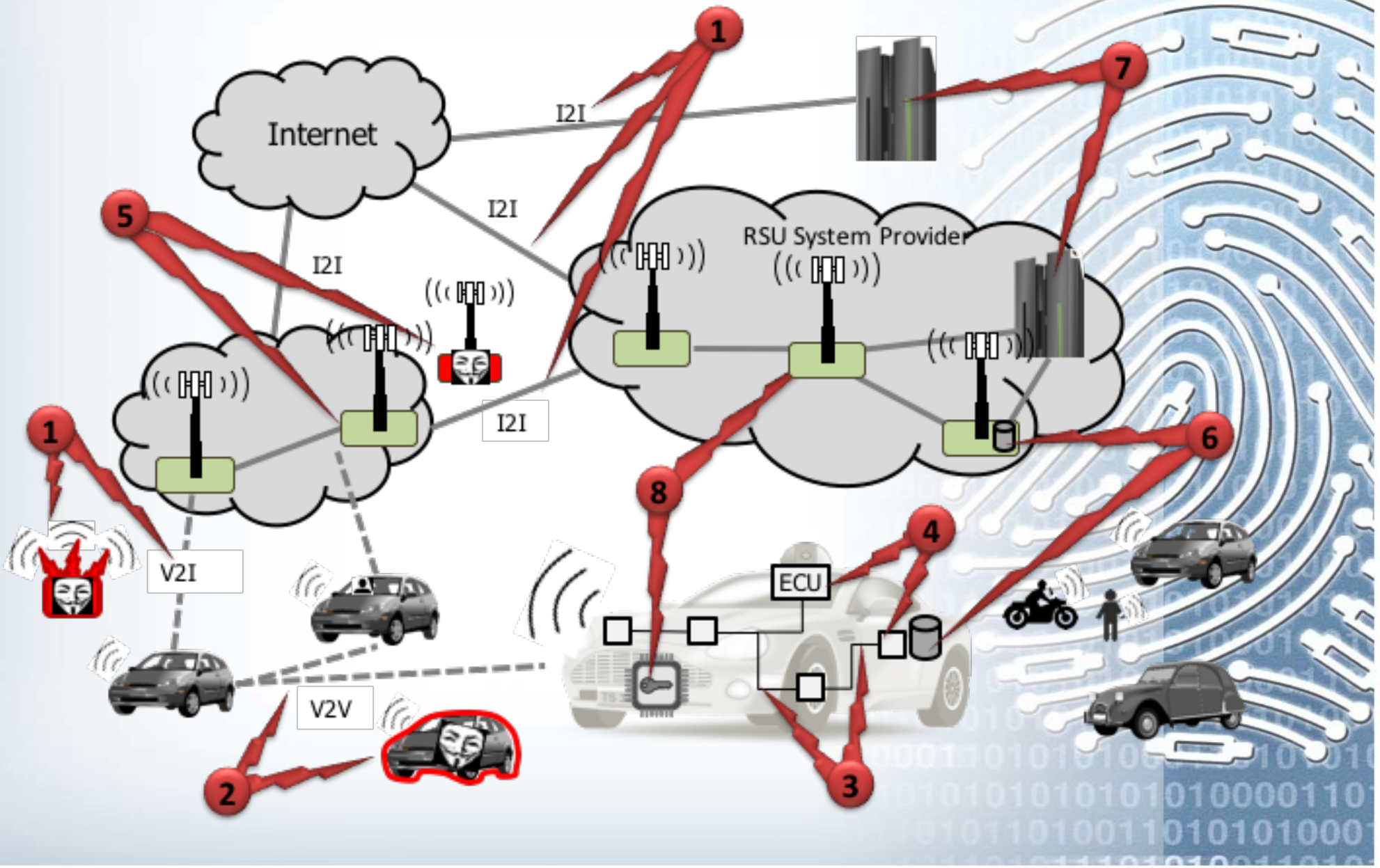
The safety-security gap in vehicle ecosystems

Vulnerabilities in a car **will** lead, rather sooner than later, to catastrophic failures;



Towards Safe and Secure Autonomous and Cooperative Vehicle Ecosystems. Lima, A; Rocha, F; Volp, M; Verissimo, P. in Proc's 2nd ACM Workshop on Cyber-Physical Systems Security and PrivaCy (2016, October) @CCS, Vienna-Austria

Autonomous vehicle ecosystem threat plane



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Critical and Extreme Security and Dependability

We're hiring bright PhD students and willing to address these challenges!

Thank you!